FIG. 1

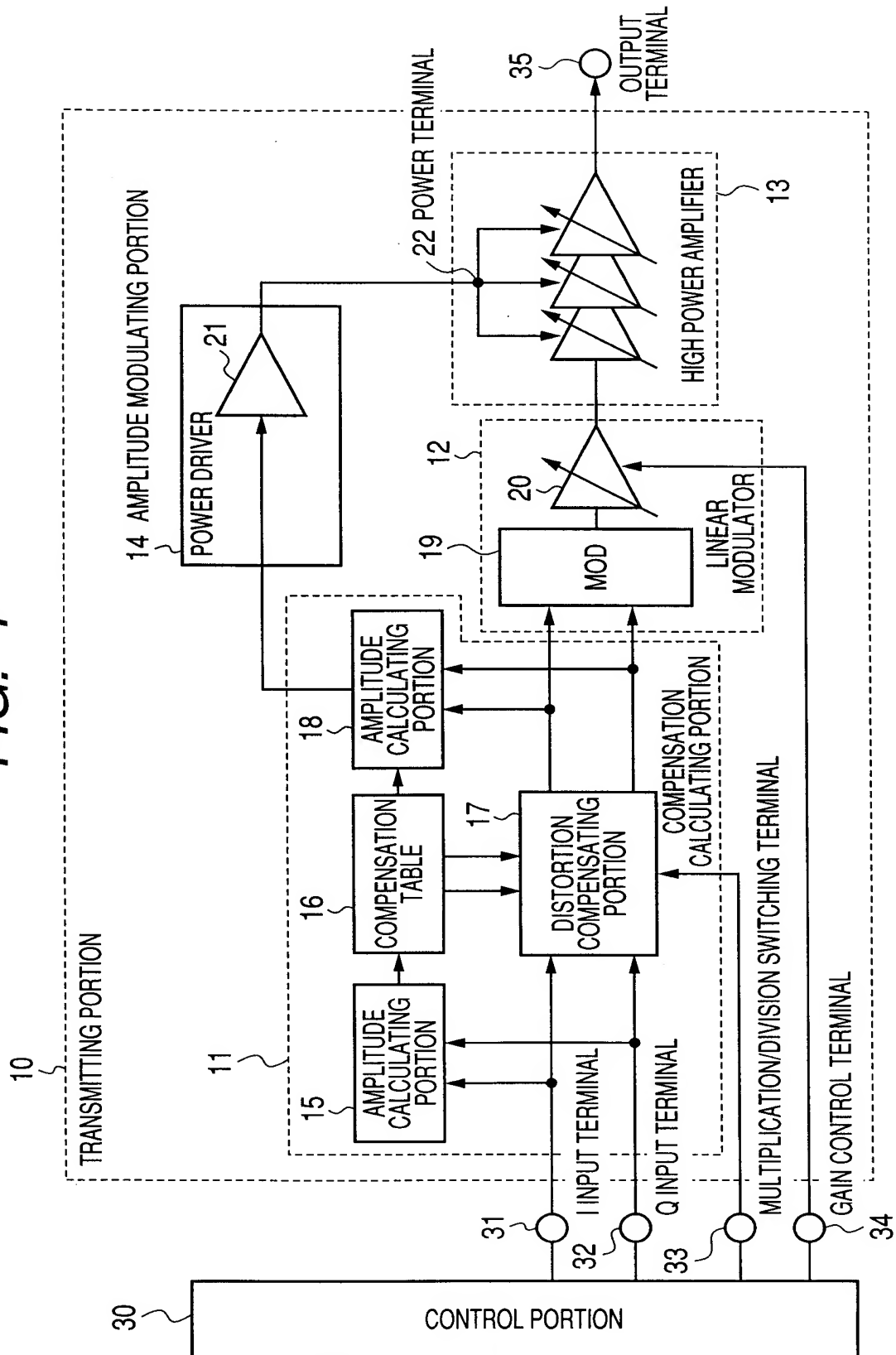## FIG. 2

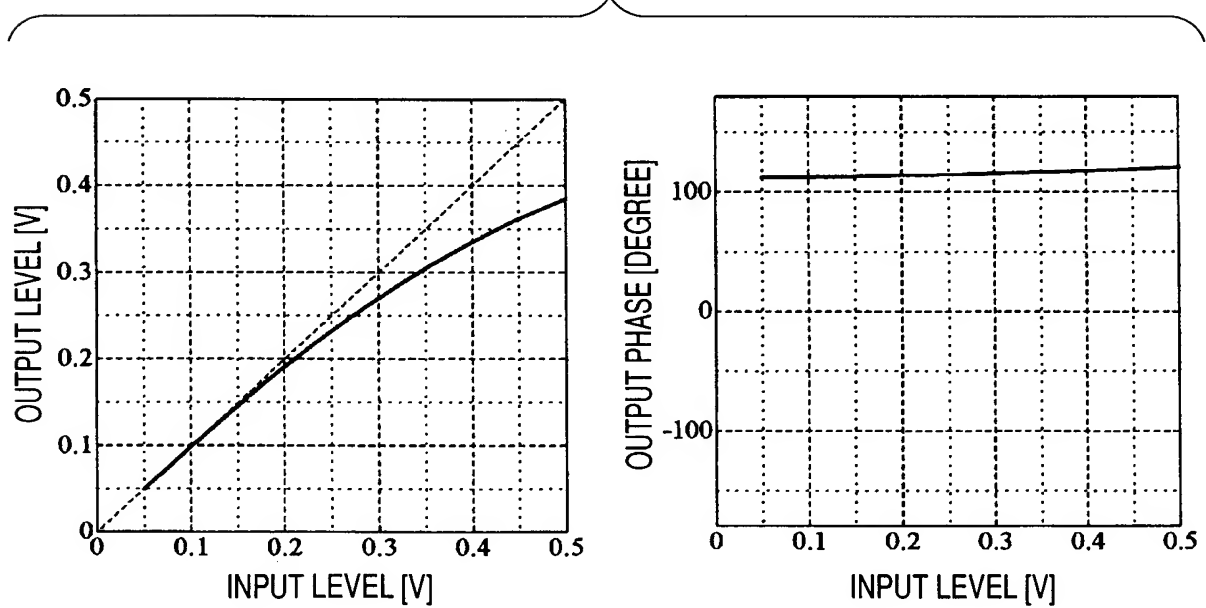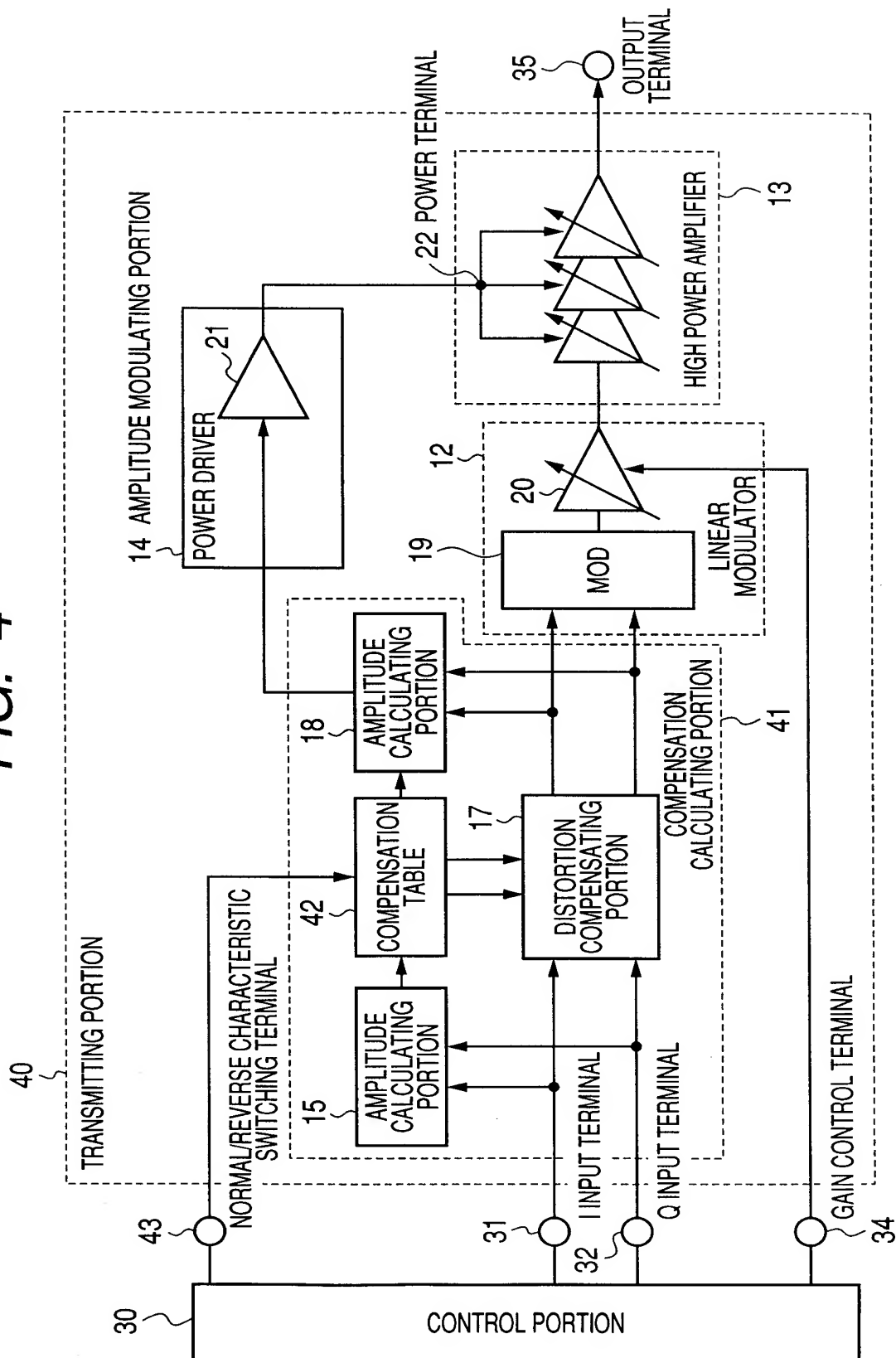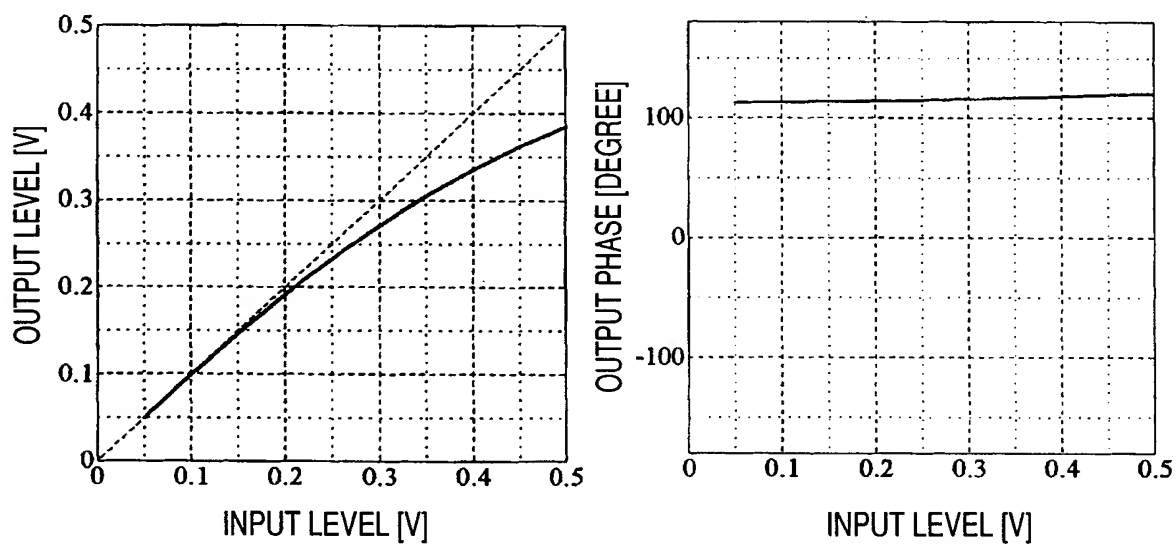| INPUT LEVEL	DISTORTION DATA (OUTPUT LEVEL)	DISTORTION DATA (PHASE)
— — —	— — —	— — —

FIG. 3

FIG. 4

FIG. 5

NORMAL CHARACTERISTIC		
INPUT LEVEL	DISTORTION DATA (OUTPUT LEVEL)	DISTORTION DATA (PHASE)
— — —	— — —	— — —
REVERSE CHARACTERISTIC		
INPUT LEVEL	DISTORTION DATA (OUTPUT LEVEL)	DISTORTION DATA (PHASE)
— — —	— — —	— — —

FIG. 6(A)
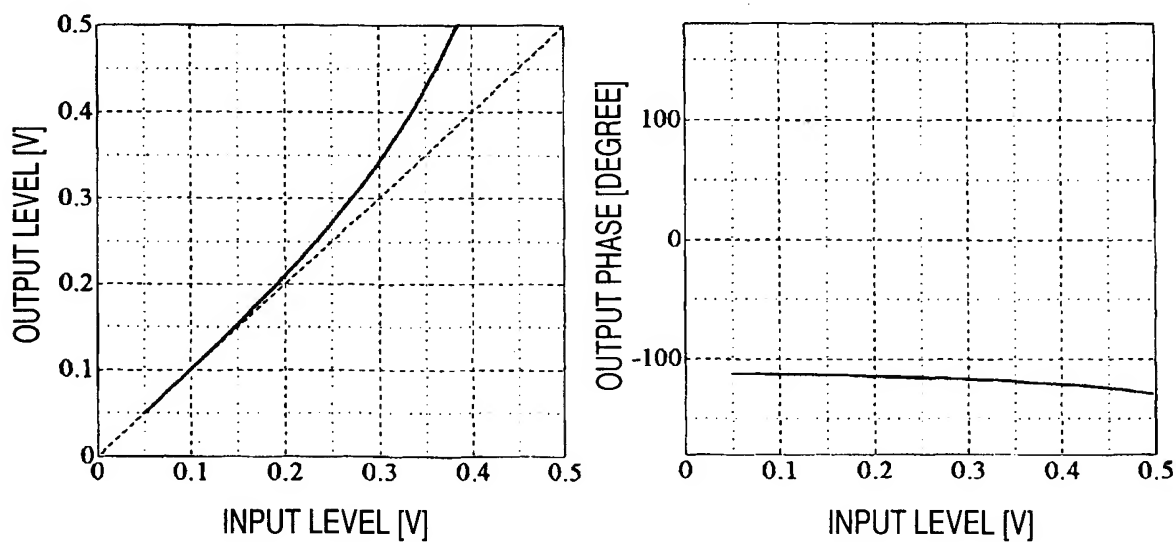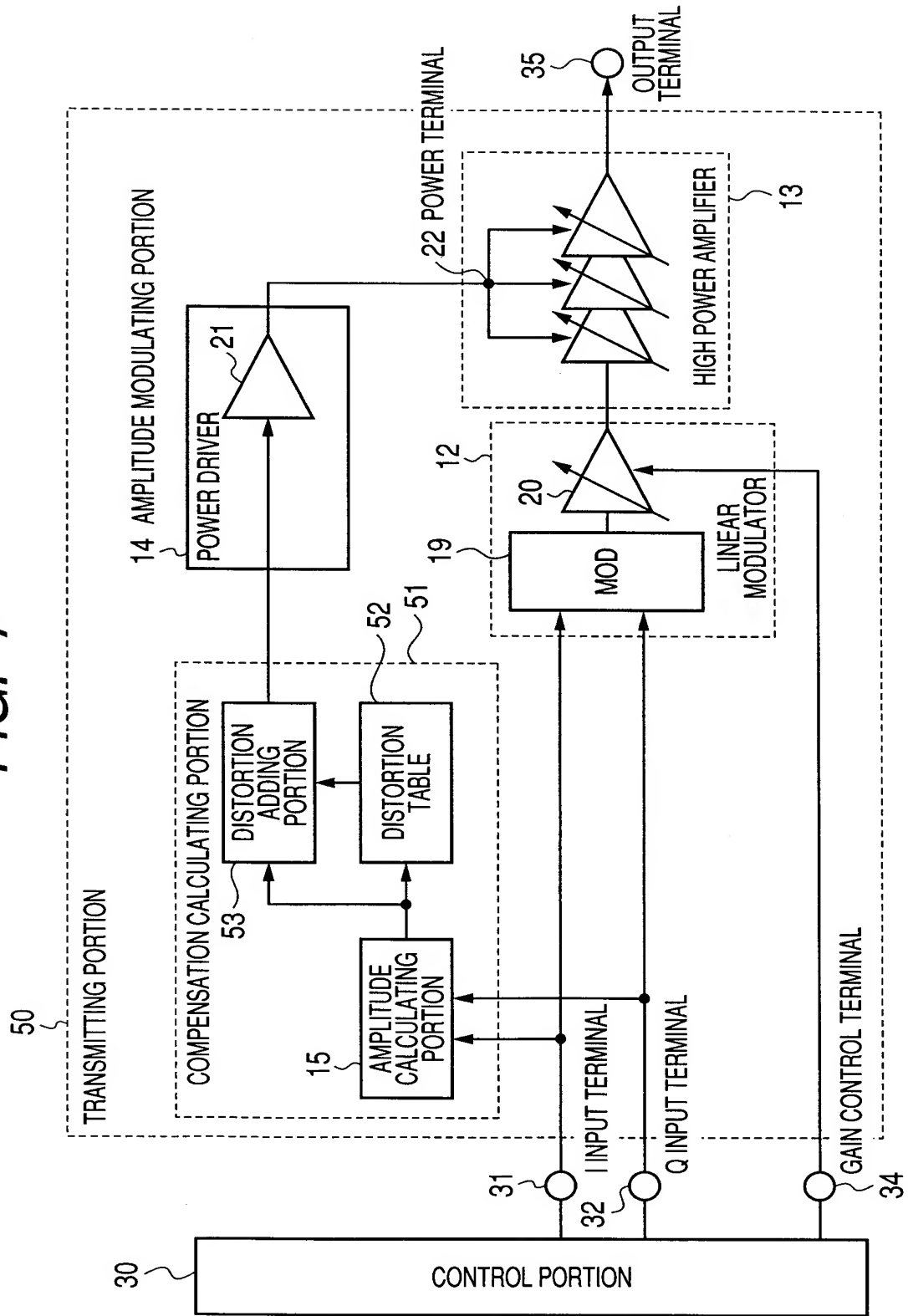NORMAL CHARACTERISTIC

FIG. 6(B)
REVERSE CHARACTERISTIC

FIG. 7

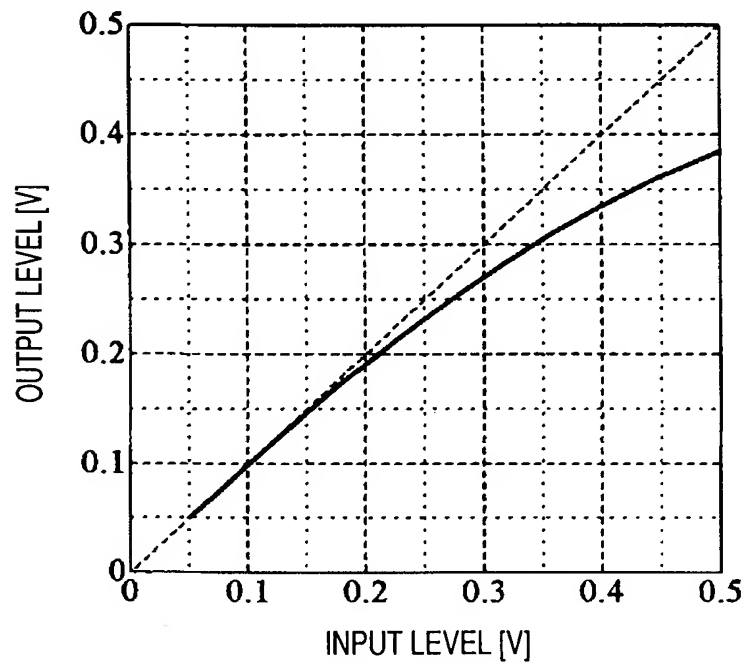# FIG. 8

INPUT LEVEL	DISTORTION DATA (OUTPUT LEVEL)
– – –	– – –

FIG. 9

FIG. 10

TRANSMITTING PORTION 60

AMPLITUDE MODULATING PORTION

14 AMPLITUDE MODULATING PORTION

POWER DRIVER 21

22 POWER TERMINAL

HIGH POWER AMPLIFIER 13

35 OUTPUT TERMINAL

AMPLITUDE DETECTING PORTION 62

20

61

MOD 19

31 I INPUT TERMINAL

32 Q INPUT TERMINAL

63 DISTORTION CONTROL TERMINAL

34 GAIN CONTROL TERMINAL

30 CONTROL PORTION

FIG. 11

FIG. 12